Digital Governance
Speakers for December 5, 2020

Class 6 "Designing Architecture as Social Infrastructure"

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Mr. Satyanarayana is a thought leader and pioneer in the areas of e-Governance, Enterprise Architecture and Digital Transformation. For over 40 years, he has served as a civil servant, including Secretary, Department of Electronics & IT, Government of India and Chairman, Unique Identification Authority of India. As the founder-CEO of National Institute for Smart Government (2003-08), led ICT Projects and Capacity Building Programs across India. Chaired the National Committees that prepared the India Enterprise Architecture (2017), Digital Service Standard (2019), and the National Digital Health Blueprint (2019). Holds a Master in physics and an MBA.

Yutaka Saito
齊藤 裕

President, Digital Architecture Design Center, Information-technology Promotion Agency, Japan / Senior Executive Vice President, Fanuc
After graduating from the University of Tokyo, Mr. Saito joined Hitachi in 1979. As a control system engineer, developed various control systems for steel plants and others. In 2014 - 18, supervised telecommunication systems as Executive Vice President. In 2018, joined FANUC and presently leads the company's IoT business as Senior Executive Vice President. In May, 2020, President, DADC, IPA.

IPAデジタルアーキテクチャ・デザインセンター所長/ファナック取締役副社長執行役員
東京大学卒業後、1979年日立製作所入社。制御システムのエンジニアとして鉄鋼関連など様々な制御システムを開発。2014年から副社長として情報通信システム事業を管掌。2018年ファナックに転社し副社長としてIoTビジネスを管掌。2020年5月にDADCセンター長就任。

For details: http://www.pp.u-tokyo.ac.jp/events/2020-10-16-27279/